



## AVANTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY

(Approved By AICTE, Permanently Affiliated to JNT University, Kakinada,  
Accredited by NAAC & Recognized Under 2(f) and 12 (b) by UGC, New Delhi)  
Tamaram, Makavarapalem, Narsipatnam(R D), Visakhapatnam Dist-531113

### Orientation Programs organized for Faculty

A.Y:2019-20

S.No	Name of the program	Resource person	Program Date	No of participants
1.	Awareness Programme on Outcome Based Education.	Dr.N.Bheema Rao Professor NIT, Warangal	25-11-2019	17
2.	Induction program for newly appointed faculty members	Dr. C P V N J Mohan Rao Principal AIET, Makavarapalem	10-02-2020	09

  
IQAC Coordinator





## **AVANTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY**

(Approved By AICTE, Permanently Affiliated to JNT University, Kakinada,  
Accredited by NAAC & Recognized Under 2(f) and 12 (b) by UGC, New Delhi)  
Tamaram, Makavarapalem, Narsipatnam(R D), Visakhapatnam Dist-531113


### **CIRCULAR**

**Dt. 23<sup>rd</sup> Nov 2019**

An Awareness program on **Outcome Based Education** for all faculty members for is scheduled from **25<sup>th</sup> November 2019** at First Year Block Seminar Hall. All the faculty members should attend the Seminar without fail. For any further information Contact Mr. M.Chiranjeevi, Assistant Professor, Computer Science and Engineering Department may be consulted.

**Resource Person – Dr. Prof. N. BHEEMA RAO, NIT Professor**

Copy to: All Departments HODs, AIET

  
**Principal**  
**Avanathi Institute of Engg. & Technology**  
**Tamaram, Makavarapalem Md.,**  
**Visakhapatnam District., Pin: 531113**



# **Prof. N. BHEEMA RAO**

**Professor**

**Department of Electronics & communication Engineering**

**National Institute of Technology, Warangal - 506004, Telangana, INDIA**

: nbr@nitw.ac.in, nbr.rao@gmail.com

: 8702462432

**Interests:** On chip passive components for RF Applications, Device Modelling and Interconnect Modelling

## **Educational Details**

- Ph.D. (2008): Indian Institute of Technology, Bombay-INDIA.
- M.E (1991): Andhra University, Vishakhapatnam-INDIA.
- B.E. (1986): Andhra University, Vishakhapatnam-INDIA.

## **Work Experience**

- Professor, From April 2018 to Till date, Dept. of ECE, National Institute of Technology, Warangal, Telangana-INDIA.
- Associate Professor, From 2008 to till March 2018, Dept. of ECE, National Institute of Technology, Warangal, Telangana-INDIA.
- Assistant Professor, From 1993 to 2008, Dept. of ECE, National Institute of Technology, Warangal, Telangana-INDIA.
- Assistant Professor, From 1987 to 1993, Dept. of ECE, GITAM University, Vishakhapatnam, A.P-INDIA.

## Course Taught Previously

### Courses Currently Being Taught

- Electronic Devices and Circuits-I
- Electronic Devices and Circuits-II
- Device Modelling
- Physical Design Automation
- VLSI System Design

### Course Taught Previously

- EM Field Theory
- Antennas and Wave Propagation
- Switching Theory and Logic Design
- Pulsed Circuits
- Electronic Instrumentation

### Laboratories

- Physical Design Automation Lab
- Electronic Devices and Circuits Lab

## Publications

### International Journals

- S. Subba Rao and N. Bheema Rao, "Analog/RF performance of Graded Channel Gate Stack Triple Material Double Gate strained-Si MOSFET with fixed charges", **Silicon, Springer**, 2021.
- S. Subba Rao and N. Bheema Rao, "Analog/RF performance of strained-Si Graded Channel Dual Material Double Gate MOSFET with interface charges", **Journal of Computational Electronics**, Vol. 20, Issue 1, pp. 492-502, 2021.
- S. Subba Rao and N. Bheema Rao, "Analytical modeling of subthreshold current and swing of strained-Si graded channel dual material double gate MOSFET with interface charges and analysis of circuit performance", **International Journal of Numerical Modelling: Electronic Networks, Devices and Fields**, Vol. 34, Issue 1, 2021.
- S. Subba Rao and N. Bheema Rao, "A Center Potential based Threshold Voltage Model for Graded-Channel Dual-Material Double Gate Strained-Si MOSFET with Interface Charges", **Journal of Computational Electronics**, Vol. 18, Issue 4, pp. 1173-1181, 2019.
- Padavala Akhendra Kumar, Narayana Kiran Akondi, and Bheema Rao Nistala. "High quality factor fractal inductor with complementary split-ring array inclusion." **Circuit World** (2020).
- Sunil Kumar Tumma, Bheema Rao Nistala, (2020) " Design of high performance narrowband low noise amplifier using an orthogonal series stacked differential fractal inductor for 5G applications ", **Turkish Journal of Electrical Engineering & Computer Sciences**. DOI: 10.3906/elk-elk-1903-185.
- Sunil Kumar Tumma, Bheema Rao Nistala, "A novel hybrid series stacked inductor differential fractal inductor for MMIC applications ", **Circuit World (Emerald Publishing)**, 2019. DOI: 10.1108/CW-06-2019-0058.
- Sunil Kumar Tumma, Bheema Rao Nistala, "High-quality factor multipath differential fractal inductor for wireless applications", **Circuit World (Emerald Publishing)**, 2021.
- Shashank Rebelli and N.Bheema Rao, "A Multiresolution Time Domain (MRTD) Method for Crosstalk Noise Modeling of CMOS-Gate-Driven Coupled MWCNT Interconnects," in **IEEE Transactions on Electromagnetic Compatibility**. 2019, 10.1109/TEM.2019.2903728.
- Shashank Rebelli and N.Bheema Rao, "An Efficient MRTD Model for the Analysis of Crosstalk in CMOS-Driven Coupled Cu Interconnects." **Radioengineering** 27.2 (2018).



- Shashank Rebelli and N.Bheema Rao, "A Novel MRTD model for Signal Integrity Analysis of Resistive Driven Coupled Copper Interconnects", **The international journal for computation and mathematics in electrical and electronic engineering**, 2017.
- P Akhendra Kumar and N.Bheema Rao, "A Novel Fractal Stacked Inductor Using Modified Hilbert Space-Filling Curve for RFICs." **Microelectronics, Electromagnetics, and Telecommunications**. Springer, Singapore, 2018. 535-540.
- P Akhendra Kumar and N.Bheema Rao, "Design of an on-chip Hilbert fractal inductor using an improved feed-forward neural network for Si RFICs." **Turkish Journal of Electrical Engineering & Computer Sciences** 26.5 (2018): 2437-2447.
- P. Akhendra Kumar and N. Bheema Rao, "Fractal series stacked inductor for radio frequency integrated circuit applications," in **Electronics Letters**, vol. 53, no. 20, pp. 1387-1388, 28 9 2017. DOI: 10.1049/el.2017.2623
- P Akhendra Kumar and N.Bheema Rao, "Fractal Parallel Stacked Inductor using Modified Hilbert Space-Filling Curve for RFIC's." **Communications on Applied Electronics**, Volume 7 - No. 5, August 2017. 10.5120/cae2017652660
- P Akhendra Kumar and N.Bheema Rao, "Design of Low Pass Filter using Integrated Fractal Passive Components for GSM Application." (2017). **Communications on Applied Electronics**, Volume 7 - No. 10, December 2017. 10.5120/cae2017652725
- P Akhendra Kumar and N.Bheema Rao, "High inductance fractal inductors for Wireless Applications", **Turkish Journal of electrical engineering and computer sciences**, 2017.
- P Akhendra Kumar and N.Bheema Rao, "Fractal Spiral Capacitor for Wireless Applications", **IET Electronics Letters**, Vol.52, No.6,2016, 10.1049/el.2015.3420.
- Nagesh.Deevi and N.Bheema Rao, "Miniature on-chip bandpass filter for RF Applications", **Springer Microsystem Technologies**, Vol.23, No.3, 2016, DOI:10.1007/s0054.
- Nagesh.Deevi and N.Bheema Rao, "Optimized fractal inductor for RF Applications", **ARPJ Journal of Engineering and Applied sciences**, Vol 11, No 5, March-2016, pp.3228-3232.
- Nagesh.Deevi and N.Bheema Rao, "Analysis of Miniature on-chip 3-D inductor for RF Applications", **ARPJ Journal of Engineering and Applied sciences**, Vol 11, No 5, March-2016, pp.3276-3279.
- Nagesh.Deevi and N.Bheema Rao, "Multi-layer On-Chip Inductor for 10-100 GHz Frequency Applications", **IET-Electronics Letters**, 2015 Vol. 51 No. 3 pp. 270-272.
- Nagesh.Deevi and N.Bheema Rao, "Parameter Optimized High Q On-chip Inductor for RF applications", **International Journal of Electrical Electronics & Telecommunication Engineering**, November-2104, ISSN:2051 -3240, Vol.45, Issue.2 Pg.No:1476-1479.
- M.Kavicharan, N.S.Murthy and N. Bheema Rao, "Efficient Delay and Crosstalk Estimation Models for Current-Mode High Speed Interconnects Under Ramp Input", **Journal of Circuits, Systems, and Computers**, Vol. 23, No. 6 (2014) 1450082 (19 pages)
- M.Kavicharan, N.S.Murthy and N. Bheema Rao "FDTD-based Time Domain Analysis of CMOS Gate Driven Interconnects", **Journal of Electronic Design Technology**, ISSN: 2229-6980 Volume 5, Issue 1.

- Keerti Kumar K, Bheema Rao N, "Power gating technique using FinFET for minimization of Sub-threshold leakage current", *Journal of Circuits, Systems, and Computers*, World Scientific Publishing Group, Singapore. Vol. 23, No. 8, September 2014.
- Keerti Kumar K, Bheema Rao N, "Sub-threshold Leakage Current Reduction Using Variable Gate Oxide Thickness (VGOT) MOSFET", *Microelectronics and Solid-State Electronics Journal*, Volume 2, No.2 (2013), Scientific and Academic Publishers, USA pp. 24-28.
- Keerti Kumar K, Bheema Rao N, "Study of Sub-threshold Leakage Currents for 32nm, 45nm, 90nm, 130nm Technologies using Variable Gate Oxide Thickness (VGOT) MOSFET", *AMSE (Association for the Advancement of Modelling and Simulation Techniques in Enterprises) Journal*, France. (Present volume 2012: 85).
- Keerti Kumar K, Bheema Rao N, "Low Leaky 4 bit Combinational Multiplier using Variable Gate Oxide Thickness (VGOT) MOSFET for Sub-threshold leakage minimization", *AMSE (Association for the Advancement of Modelling and Simulation Techniques in Enterprises) Journal*, France. (Present volume 2012: 85)
- N. Bheema Rao and A.N.Chandorkar "3 D Inductor for RF Applications," *International Journal of Microwave and Optical Technology*, (IJMOT), April 2008.
- N. Bheema Rao and A.N.Chandorkar "Inductance Optimization by Turn Segment Tapering for RF Applications," *Journal of Modeling-A (ASME)*, vol. 80, no. 4, pp. 48-58, 2007.
- N. Bheema Rao and A.N.Chandorkar "Optically Tunable Multi Turn Spiral Inductor for RF Applications," *Microwave and Optical Technology Letters*, vol 46, July 2005.

#### **International Conferences**

- M.V. Raghunadh and Dr. N. Bheema Rao, "High Performance double split series stacked multilayer On-chip Inductor for 5G applications", Springer International Conference, **OWT 2019**, MNIT, Jaipur, 25-27 March 2019.
- M. V. Raghunath and Dr. N. Bheema Rao, "A compact IPD based on chip Band Pass Filter for 5G Radio Applications", Springer International Conference, **IC2SV 2019**, NIT, Warangal, 24 OCT 2019.
- M. V. Raghunath, Dr. N. Bheema Rao, "A High Performance Miniaturized on-chip 25 GHz Narrow Band Pass Filter for 5G Radio Access Applications", IEEE International Conference, **ICRAIE 2019**, Universiti Teknologi, Kedah, Malaysia, 27-29 NOV 2019.
- M. V. Raghunath, Dr. N. Bheema Rao, "A Compact Low Loss on-chip-BPF for 5G Radio Front End using IPD Technology", IEEE International Conference, **ISMAL 2019**, SCAD Institute of Technology, Coimbatore, 12-14 DEC 2019.
- Sunil Kumar Tumma, Bheema Rao Nistala, "Series stacked non-parallel multipath differential inductor for C band applications," **Springer Lecture Notes in Electrical Engineering (LNEE), Volume 686, Book chapter.**
- P Akhendra Kumar and N.Bheema Rao, "Design of Low Pass Filter using Integrated Fractal Passive Components for GSM Application." *Communications 7 (2017):* 1-3.
- Sunil Kumar Tumma and N.Bheema Rao, "High performance variable-width multipath multilayer differential inductor", IEEE India international conference (INDICON), IIT Roorkee, 15th-17th December 2017.
- Sunil Kumar Tumma and N.Bheema Rao, "A Novel High Q Variable Width Differential Series Stacked Spiral Inductor", IEEE International conferences on Computing, Communication and Network Technologies (ICCCNT), IIT Delhi, India, 3rd-5th July 2017.



- P Akhendra Kumar and N.Bheema Rao, "High Quality factor on-chip inductor using modified Hilbert space-filling curve for Radiofrequency applications", 5th International Conference on Computing, Communications and Sensor Network (CCSN-2016) during 24-26 th, Dec 2016.
- P Akhendra Kumar and N.Bheema Rao "Fractal spiral capacitor for RF applications ", IEEE International Conference on Control, Measurement and Instrumentation", (CMI), Kolkata India, 2016.
- P Akhendra Kumar and N.Bheema Rao," Design of Multilayer fractal inductor for RF Applications", IEEE International Conference on Microwave, Optical and Communication Engineering(ICMOCE-2015) at IIT-B, Bhubaneswar during 18th-20th Dec 2015.
- P Akhendra Kumar and N.Bheema Rao "Differential pair fractal inductor for Wireless Applications "Proceedings of International Conference on electromagnetic Interference & Compatibility ",(INCEMIC 2015), Vishakhapatnam, India, 2015
- P Akhendra Kumar and N.Bheema Rao "A Novel fractal inductor for RF Applications", IEEE International Conference on Industrial Instrumentation and Control (ICIC), Pune, India, 2015 Pages: 857 - 859, DOI:10.1109/IIC.2015.7150862.
- Nagesh. Deevi and N.Bheema Rao, "Extraction of on-chip inductor value using McLaren's method for RF Applications", International Conference on Emerging Technologies in Electronics & Communication-ICE IEC-13, at Guru Nanak Dev University, Amritsar during 20-22 December 2013.
- Deevi and N.Bheema Rao, "Optimized On-chip Inductor with high Q-factor", International Conference on Microelectronics, Circuits, and Systems-Micro",2014, at Kolkata during 11-13 July 2014.
- Nagesh Deevi and N.Bheema Rao, "On-Chip Spiral Inductor for Filter Applications", 3<sup>rd</sup> International Conference on Computing, Communication and Sensor Network at Hotel Chankya, PURI during 12-14 December 2014.
- Deevi and N.Bheema Rao, "High Q-factor On-chip Spiral Inductor for Wireless applications",8th Annual International Conference Antenna Test & Measurement Society-2015 at Bangalore during 3-4 February 2015.
- Deevi and N.Bheema Rao, "Multilayer Grown High-Q On-chip Inductor for RF Applications", 2015 3rd IEEE-International Conference on Signal Processing, Communication and Networking (ICSCN-15) at Madras Institute of Technology, Anna University, Chennai during 26-28 March 2015.
- Deevi and N.Bheema Rao, "Design of Multilayer On-chip Inductor with Low K-Dielectrics for RF applications", 2015 3rd IEEE-International Conference on Signal Processing, Communication and Networking (ICSCN-15) at Madras Institute of Technology, Anna University, Chennai during 26-28 March 2015.
- Deevi and N.Bheema Rao, "Effect of Conductor Thickness on On-Chip 3-D Inductor for Wireless Applications", IEEE International Conference on Industrial Instrumentation and Control (ICIC 2015) at College of Engineering, PUNE during 28-30 May 2015
- Deevi and N.Bheema Rao, "Optimized Fractal Inductor for RF Applications", International Conference on Telecommunications, Electronic and Computer Engineering (ICTEC-2015) at Mahkota Hotel, Melaka, Malaysia during 16-17 November 2015.
- Deevi and N.Bheema Rao, " Analysis of Miniature On-Chip 3-D Inductor for RF Circuits", International Conference on Telecommunications, Electronic and Computer Engineering (ICTEC-2015) at Mahkota Hotel, Melaka, Malaysia during 16-17 November 2015.
- Kavicharan, N.S.Murthy and N. Bheema Rao "Modal Decomposition-based VLSI Interconnect Delay Modeling", 2012 International Conference on Solid-State and Integrated Circuit (ICSIC 2012) IPCSIT vol. 32 (2012), IACSIT Press, Singapore.
- Kavicharan, N.S.Murthy and N. Bheema Rao, "A Closed-form Delay Estimation Model for Current-mode High Speed VLSI Interconnects" Proceedings of The International Conference on Technological Advances in Electrical, Electronics and Computer Engineering (TAECE 2013) Konya, Turkey, May 9-11, 2013.
- Kavicharan, N.S.Murthy and N. Bheema Rao, "An Efficient Delay Estimation Model for High Speed VLSI Interconnects, "International Workshop on Advances in VLSI Circuit Design and Cad Tools (AVCDCT13) Mysore, India, 2013.

- Kavicharan, N.S.Murthy and N. Bheema Rao, "A Novel Delay and Overshoot Estimation model for VLSI Global Interconnects", Advances in Robotics, Mechatronics and Circuits, ISBN: 978-1-61804-242-2, Pg: 272-276.
- Kavicharan, N.S.Murthy and N. Bheema Rao, "An Efficient Hybrid Voltage/Current mode Signaling Scheme for On-Chip Interconnects", Advances in Robotics, Mechatronics and Circuits, ISBN: 978-1-61804-242-2, Pg:251-256.
- Kavicharan, N.S.Murthy and N. Bheema Rao, "An Efficient Distributed Tree Structure Modelling for VLSI circuits" Recent Advances in Electrical and Computer Engineering, ISBN: 65 978-1-61804-228-6, Pg: 61-65.
- Keerti Kumar K, Bheema Rao N, "Variable Gate Oxide Thickness MOSFET: A Device-level solution for sub-threshold leakage current reduction, "Proceedings of International Conference on Devices, Circuits and Systems 2012, Coimbatore.
- Keerti Kumar K, Bheema Rao N, "Characterization of Variable gate oxide thickness MOSFET with non-uniform oxide thicknesses for sub-threshold leakage current reduction" Vol.32, Proceedings of International Conference on Solid state and Integrated Circuit 2012, Singapore.
- Keerti Kumar K, Anil P, and Bheema Rao N, "Parametric Variation with Doping Concentration in a FinFET using 3D TCAD, "IJCA Proceedings on International Conference on Microelectronics, Circuits, and Systems MICRO (3):21-23, October 2014.
- Vivek Kajla and Dr. N. Bheema Rao, "TIQ Flash ADC for UWB Receiver Architecture", International Conference on Microelectronics, Circuits, and Systems-Micro-2014, at Kolkata during 11-13 July 2014.
- Srikanth Konale, N.Bheema Rao, "C-based Predictor for Scoreboard in Universal Verification Methodology" IEEE International Conference on Advances in Engineering & Technology Research (ICAETR-2014) August 01-02-2014.
- Srikanth Konale, N.Bheema Rao, "Test Bench Development using Universal Verification Methodology for Functional Verification" International Conference on Microelectronics, Circuits, and Systems-Micro-2014, at Kolkata during 11-13 July 2014
- N Bheema Rao, "Coupled Spiral Inductor for RF applications," Proceedings of International Conference on Solid state and Integrated Circuit 2012.
- N. Bheema Rao and A.N.Chandorkar "Tunable Spiral Inductor for RF Applications," International Conference on Bio-Medical Electronics and Telecommunications (BET-2004), Dec 9-11, December 2004, Visakhapatnam, INDIA.
- N. Bheema Rao and A.N.Chandorkar "Optically Tunable Bandpass filter for RF Applications," Optoelectronic and Microelectronic Materials and Devices (COMMAD-2004), Dec-2004, Australia
- N. Bheema Rao and A.N.Chandorkar "Optically Tunable Spiral Inductor for RF Application," Ninth International Conference on Communication Systems (ICCS-2004), Singapore, Sept. 2004.
- N.Bheema Rao, G.S.N Raju and K.R.Gottumukkala "Generation of Radiation Pattern for the Suppression of EMI/EMC in Radar Receivers," INCEMIC-92, Dec 2-4, 1992, Calcutta, INDIA

## PHDs Supervised

- Six Ph. D's are awarded as a Supervisor.
- Guiding Two full-time Ph. D scholars.
- Guiding four part-time Ph. D scholars.
- Examiner for M.Tech and Ph. D theses in various Universities.



## **Workshops/Conferences**

- An Electronics & ICT Academy Sponsored Course on "Electronic Devices and Circuits", from 1st - 6th June, 2016 organized Department of Electronic and Communication Engineering, NITW and GITAM University, Hyderabad.
- A MHRD Sponsored Course on "Analysis and Design of Electronic Circuits" from 29th June to 11th July 2009.
- A MHRD Sponsored Course on "Electronic Devices and Circuits" from 25th August to 6th September 2008.

## **Projects**

## **Awards and Honors**

## **Additional Responsibility**

1. Head of the ECE department from 1/11/2017 to 26/12/2019
2. Head computer center during 2013-15
3. Mess Warden for 4th Mess
4. Worked ECE association adviser



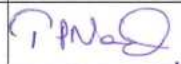
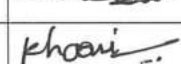
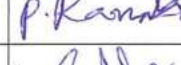



## AVANTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY

(Approved By AICTE, Permanently Affiliated to JNT University, Kakinada,  
Accredited by NAAC & Recognized Under 2(f) and 12 (b) by UGC, New Delhi)  
Tamaram, Makavarapalem, Narsipatnam(R D), Visakhapatnam Dist-531113

### Awareness Programmed on Outcome Based Education

**Resource person:** Dr.N.Bheema Rao,Professor,NIT, Warangal

**Date:** 25-11-2019

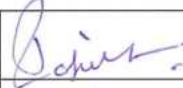
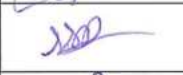
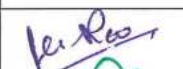
S.No	Name of the Faculty	Department	Designation	Signature
1	PATTALA T NAIDU	ECE	ASST PROFESSOR	
2	RANGANATH M SARAT	ECE	ASST PROFESSOR	
3	D SRINIVASA RAO	ECE	ASST PROFESSOR	
4	ANEEL R KUMAR	ECE	ASST PROFESSOR	
5	SISTLA VARAPRASAD PHANI	ECE	ASST PROFESSOR	
6	AJITHA BELLAPUKONDA TEJA	ECE	ASST PROFESSOR	
7	A N S SURYA PRAKASH	MECH	ASST PROFESSOR	
8	PITTA RAMA KRISHNA	MECH	ASST PROFESSOR	
9	SADHANA PALLETI	MECH	ASST PROFESSOR	
10	TRINADHA VAKADA	CSE	ASST PROFESSOR	
11	NAGARJUNA GUDELLI RAO	CSE	ASST PROFESSOR	
12	DABIBIRU PRASANNA KUMARI	CSE	ASST PROFESSOR	
13	VENKATA SIYYADULA RAMA VARAPRASAD	CSE	ASST PROFESSOR	
14	VARAHALA PETLA DORA	EEE	ASST PROFESSOR	





## AVANTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY

(Approved By AICTE, Permanently Affiliated to JNT University, Kakinada,  
Accredited by NAAC & Recognized Under 2(f) and 12 (b) by UGC, New Delhi)  
Tamaram, Makavarapalem, Narsipatnam(R D), Visakhapatnam Dist-531113

15	G RAJASEKHAR YADAV	EEE	ASST PROFESSOR	
16	SUJATHA SEELAM DEVI	EEE	ASST PROFESSOR	
17	NARYANA KOLAGANI RAO	EEE	ASST PROFESSOR	

  
Principal  
Avanathi Institute of Engg. & Technology  
Tamaram, Makevarapalem Md.,  
Visakhapatnam District., Pin: 531113



## AVANTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY

(Approved By AICTE, Permanently Affiliated to JNT University, Kakinada,  
Accredited by NAAC & Recognized Under 2(f) and 12 (b) by UGC, New Delhi)  
Tamararam, Makavarapalem, Narsipatnam(R D), Visakhapatnam Dist-531113

### CIRCULAR

**Dt. 08<sup>th</sup> Feb 2020**

A Technical Seminar on *Induction program for newly appointed faculty members* for all the faculty members during the academic year 2019-2020 is scheduled from **10<sup>th</sup> February 2020** at First Year Block Seminar Hall. All faculty members attend the Seminar without fail. For any further information Contact Mr. N. V. Ashok Kumar, Assistant Professor, Computer Science and Engineering Department may be consulted.

**Resource Person – Dr. C.P.V.N.J. Mohan Rao, Principal, AIET**

**Principal**  
**Principal**

**Avanathi Institute of Engg. & Technology**  
**Tamararam, Makavarapalem Md.,**  
**Visakhapatnam District., Pin: 531113**

Copy to: All the Department HODs, AIET.






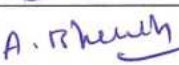
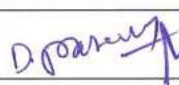
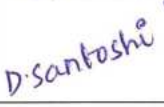
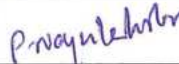

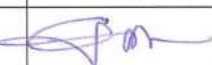
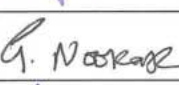

## AVANTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY

(Approved By AICTE, Permanently Affiliated to JNT University, Kakinada,  
Accredited by NAAC & Recognized Under 2(f) and 12 (b) by UGC, New Delhi)  
Tamaram, Makavarapalem, Narsipatnam(R D), Visakhapatnam Dist-531113

### Induction program for newly appointed faculty members

**Resource person:** Dr. C P V N J Mohan Rao, Principal, AIET, Makavarapalem

**Date:** 10-02-2020

S.No	Name of the Faculty	Department	Designation	Signature
1	Dr SURESH CHALLA	MECH	ASSOC PROFESSOR	
2	BHARATH ANDRA KUMAR	CSE	ASST PROFESSOR	
3	DABBIRU PRASANNA KUMARI	CSE	ASST PROFESSOR	
4	BALA SANTHOSHI DHANYAMARAJU MATHA	CSE	ASST PROFESSOR	
5	NAGA LAKSHMI PEELA	CSE	ASST PROFESSOR	
6	SANDHYA GONDASU	ECE	ASST PROFESSOR	
7	GANESH KVS	ECE	ASST PROFESSOR	
8	NOOKA RAJU GOURIPATTAPU	B&S	ASST PROFESSOR	
9	SREERAM KRISHNAVENI	B&S	ASST PROFESSOR	



Principal

Principal

Avanthi Institute of Engg. & Technology  
Tamaram, Makavarapalem Md.,  
Visakhapatnam District., Pin: 531113



## AVANTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY

(Approved By AICTE, Permanently Affiliated to JNT University, Kakinada,  
Accredited by NAAC & Recognized Under 2(f) and 12 (b) by UGC, New Delhi)  
Tamaram, Makavarapalem, Narsipatnam(R D), Visakhapatnam Dist-531113

### Orientation Programs organized for Students

A.Y:2019-20

S.No	Name of the program	Resource person	Program Date	No of participants
1	Emerging Trends in Neural Networks and Soft Computing	Dr.CPVNJ Mohan Rao ,AIET, Principal	14.02.2020	58
2	Project orientation for MBA Students through online	Dr.S G Ramarao, Asst professor, GITAM School of management studies	01-09-2020	51

  
IQAC Coordinator







## **AVANTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY**

(Approved By AICTE, Permanently Affiliated to JNT University, Kakinada,  
Accredited by NAAC & Recognized Under 2(f) and 12 (b) by UGC, New Delhi)  
Tamaram, Makavarapalem, Narsipatnam(R D), Visakhapatnam Dist-531113

### **CIRCULAR**

**Dt. 12<sup>th</sup> Feb 2020**

An Awareness program on **Emerging Trends in Neural Networks and Soft Computing** for all students of academic year 2019-2020 for is scheduled from **14<sup>th</sup> Feb 2020** at First Year Block Seminar Hall. All the students should attend the Seminar without fail. For any further information Contact Mr. N. V. Ashok Kumar, Assistant Professor, Computer Science and Engineering Department may be consulted.

**Resource Person – Dr.C.P.V.N.J Mohan Rao, Principal, AIET**

**Principal**  
**Principal**  
**Avanathi Institute of Engg. & Technology**  
**Tamaram, Makavarapalem Md.,**  
**Visakhapatnam District., Pin: 531113**

Copy to: All Departments HODs, AIET



## AVANTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY

(Approved By AICTE, Permanently Affiliated to JNT University, Kakinada,  
Accredited by NAAC & Recognized Under 2(f) and 12 (b) by UGC, New Delhi)  
Tamaram, Makavarapalem, Narsipatnam(R D), Visakhapatnam Dist-531113

### Emerging Trends in Neural Networks and Soft Computing

Resource Person: Dr.CPVNJ Mohan Rao,AIET, PRINCIPAL

Date:14-02-2020

1	19811A0401	ADATRAVU BHARGAVI	A. Bhargavi
2	19811A0402	ALLU ROOPA	A. Roopa
3	19811A0403	ANIMIREDDI HYMA	A. Hyma
4	19811A0404	BAIJYAMINI	B. Yamini
5	19811A0405	BEVARA DIVYASREE	B. Divya Sree
6	19811A0406	BHAVANA BUDDA	B. Bhavana
7	19811A0407	BODAPATI SAMU SRI	B. Samu Sri
8	19811A0408	BYLAPUDI VINITH	B. Vinith
9	19811A0409	KANTIPAMU SASIKALA	K. Sasikala
10	19811A0410	CHITIKALA PRADEEPCHANDRA	C. Pradeep
11	19811A0411	CHITRADA RAMESH	C. Ramesh
12	19811A0412	CHOCHUPATLA KUSUMALATHA	C. Kusumalatha
13	19811A0413	CHUA BALAJI	Ch. Balaji
14	19811A0414	DANDA SNEHA	D. Sneha
15	19811A0415	DEENAMKONDA GOPAL RAJU	D. Gopal Raju
16	19811A0416	DEEPTHI PEDAPATI	P. Deepthi
17	19811A0417	GANISETTY DIVAYA JYOTHI	G. Divya Jyothi
18	19811A0418	GANUGULA BHARAT KUMAR	G. Bharat Kumar
19	19811A0419	GAVIREDDI DEVI	G. Devi
20	19811A0420	GEDDAM TEJASWI	G. Tejaswi
21	19811A0421	KARAKA PYDIRAJU	K. Pydiraju
22	19811A0422	KEERTHI ROHINI	K. Rohini
23	19811A0423	KEMBURU SAISREE	K. Sai Sree
24	19811A0424	KILAPARTHI SAI KANNAYYA NAIDU	K. Sai Kannayya.
25	19811A0425	KODA MANIKANTA	K. Manikanta
26	19811A0426	KOTLA KRISHNA KUMAR	K. Krishana
27	19811A0427	KAMBALA LIKHITHA	K. Likhitha
28	19811A0428	MEDE NAGA SRAVANI	M.N. Sravani
29	19811A0429	MENDI DENIL	M. Denil
30	19811A0430	MUNASAI AHEMA LATHA	M. Hema Latha
31	19811A0431	MUPPANADAM VENKATA MANIKANTA SAI	M. Venkata Manikanta



## AVANTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY

(Approved By AICTE, Permanently Affiliated to JNT University, Kakinada,  
Accredited by NAAC & Recognized Under 2(f) and 12 (b) by UGC, New Delhi)  
Tamaram, Makavarapalem, Narsipatnam(R D), Visakhapatnam Dist-531113

32	19811A0432	NADIGATLA MAHALAKSHMI	N. Mahala
33	19811A0433	NAKKA GANESH	N. Ganesh
34	19811A0434	NAKKA MANASA	N. Manasa
35	19811A0435	PADI DEVI	P. Devi
36	19811A0436	PALLADI DIVYA	P. Divya
37	19811A0437	PAMPANA MANI JYOTISNA	P.H. Jyotishna
38	19811A0438	PILLI MOHAN GOWRI KALAVATHI	P. Kalavathi
39	19811A0440	POTHUNEEDI PAAVAN KALYAN	P. Pavan
40	19811A0441	RAPETI HEERAVANI	R. Heeravani
41	19811A0443	SALADULA POORNA CHANDRASEKHAR	S. Purna Chandrasekhara
42	19811A0444	SHAIK BASHEERAMMA	S. Basheeramma
43	19811A0445	SINGAMPALLI MOHAN KISHORE	Singampalli Mohan
44	19811A0446	SUREDDI SWATHI	S. Swathi
45	19811A0447	TALARI MOHAN VASANTH KUMAR	T. Kumar
46	19811A0448	TATIKONDA NANI	T. Nani
47	19811A0449	TEPPALA TARUN	T. Tarun
48	19811A0450	TONANGI PAVANI	T. Pavani
49	19811A0451	TURUBILLI KOTESWARA RAO	T. Koteswara Rao
50	19811A0452	VUDI KIRAN	V. Kiran
51	19811A0453	YERNAGULA RAHUL	Y. Rahul
52	19811A0501	ABBURI VENKATA SURENDRA	A. Surendra
53	19811A0502	ALLADA BHARGAV SANTHOSH	A. Bhargav Santhosh
54	19811A0503	ANDUKURI VENKATALAKSHMI BHARGAVI	A. Venkata Lakshmi Bhargavi
55	19811A0504	ANNAM MOUNIKA SRI	A.M. Sri
56	19811A0505	BALIREDDI MADHU PRAKASH	B. Madhu Prakash
57	19811A0506	BATTALA LEKHYA SRI	B. Lekhya Sri
58	19811A0507	BILLA CHANDRIKA	B. Chandrika

Principal  
Principal

Avanthi Institute of Engg & Technology  
Tamaram, Makavarapalem Md.,  
Visakhapatnam District., Pin: 531113





## **AVANTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY**

(Approved By AICTE, Permanently Affiliated to JNT University, Kakinada,  
Accredited by NAAC & Recognized Under 2(f) and 12 (b) by UGC, New Delhi)  
Tamaram, Makavarapalem, Narsipatnam(R D), Visakhapatnam Dist-531113

### **CIRCULAR**

**Dt. 29<sup>th</sup> Aug 2020**

An Awareness program on **Project Orientation for MBA Students Through Online** for all MBA students of academic year 2019-2020 for is scheduled from **1<sup>st</sup> September 2020** at First Year Block Seminar Hall. All the students should attend the Seminar without fail. For any further information Contact Mr. T. Gopi, Assistant Professor, MBA may be consulted.

**Resource Person – Dr.S G Ramarao, Asst professor, GITAM School of Management Studies**

**Principal**

Avanthi Institute of Engg. & Technology  
Tamaram, Makavarapalem Md.,  
Visakhapatnam District., Pin: 531113

Copy to: MBA HOD, AIET



## AVANTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY

(Approved By AICTE, Permanently Affiliated to JNT University, Kakinada,  
Accredited by NAAC & Recognized Under 2(f) and 12 (b) by UGC, New Delhi)  
Tamaram, Makavarapalem, Narsipatnam(R D), Visakhapatnam Dist-531113

### Project orientation for MBA Studnts through online

**Resource Person:** Dr.S.G.Ramarao, Asst professor, GITAM School of management studies

Date: 01-09-2020

1	19811A0401	ABBURI VENKATA SURENDRA	A. Venkata Surendra
2	19811A0402	ALLADA BHARGAV SANTHOSH	A. Bhargav Santhosh
3	19811A0403	ANDUKURI VENKATALAKSHMI BHARGAVI	A. Venkata Lakshmi Bhargavi
4	19811A0404	ANNAM MOUNIKA SRI	A. Mounika Sri
5	19811A0405	BALIREDDI MADHU PRAKASH	B. Madhu prakash
6	19811A0406	BATTALA LEKHYA SRI	B. Lekhya Sri
7	19811A0407	BILLA CHANDRIKA	B. Chandrika
8	19811A0408	BODDETI CHANDIPRIYA	B. Chandipriya
9	19811A0409	BONDA TARUN	B. Tarun
10	19811A0410	BOTSA DURGA PRASAD	B. Durga prasad
11	19811A0411	BUDDIGA NIKHIL RAM GOUD	B. Nikhil Ram Goud
12	19811A0412	CHELLURI VASAVI	Ch. Vasavi
13	19811A0413	CHOKKAKULA BHARATHI	C. Bharathi
14	19811A0414	CHOPPA DURGALAKSHMI	C. Durgalakshmi
15	19811A0415	DABBEERU NAGALAKSHMI	D. Nagalakshmi
16	19811A0416	DONKA INDRANI	D. Indrani
17	19811A0417	DUGGIRALA SAI PAVANI JAGADEESWARI	D. Sai pavani Jagadeeswari
18	19811A0418	EATI JOSHNA	E. Joshna
19	19811A0419	GANGARAPU PAVANI	G. pavani
20	19811A0420	GATREDDI SANDHYA RENUKA	G. Sandhya Renuka
21	19811A0421	GEDELA PRADEEP	G. pradeep
22	19811A0422	GHATTA SRINIVASA RAGHAVENDRA	G. S. Raghavendra
23	19811A0423	GOGU CHANDINI	G. Chandini
24	19811A0424	MARRA MOUNICA	M. Mounica
25	19811A0425	GURRAM VENKATA ANUSHA	G. Venkata Anusha
26	19811A0426	JAVVADI RAJESH	J. Rajesh
27	19811A0427	KALAGANI JYOTHIRADITYA	K. Jyothiraditya
28	19811A0428	KAMPATI NAGADEVI	K. Nagadevi
29	19811A0429	KANCHIPATI MADHURI	K. Madhuri



## AVANTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY

(Approved By AICTE, Permanently Affiliated to JNT University, Kakinada,  
Accredited by NAAC & Recognized Under 2(f) and 12 (b) by UGC, New Delhi)  
Tamararam, Makavarapalem, Narsipatnam(R D), Visakhapatnam Dist-531113

30	19811A0430	KANDREGULA NAVEEN ASHOK	K. Naveen Ashok
31	19811A0431	KANITHI SUSHMITHA LAKSHMI	K. Sushmitha Lakshmi
32	19811A0432	KARAKAVALASA SUJIT	K. Sujit
33	19811A0433	KARRI ANUSHA	K. Anusha
34	19811A0434	KARRI JYOTHI SAI LAKSHMI	K. Jyothi
35	19811A0435	KAYALA GANGA BHAVANI	K. Ganga Bhavani
36	19811A0436	KOMMANAMANCHI TARUN ARAVIND	K. Tarun Aravind
37	19811A0437	KOMMANAMANCHI VIVEK SARMA	K. Vivek Sarma
38	19811A0438	KONDRAGUNTA TEJA	K. Teja
39	19811A0440	KOTTANA SIVA KUMAR	K. Sivakumar
40	19811A0441	KOVVADA HEMA	K. Hema
41	19811A0443	KOVVADA NAGA KEERTHI	K. Naga Keerthi
42	19811A0444	KUNDRAPU SIVAANJALINAGADEVI	K. Sivanjali
43	19811A0445	LANKA DURGA JAGADEESWARI JYOTHSNA	L. Durga Jagadeeswari Jyothsna
44	19811A0446	LEKKALA CHAITANYA KIREETI	L. Chaitanya Kireeti
45	19811A0447	LEKKALA VENKATA SAI	L. Venkata Sai
45	19811A0448	PAILA VENU GOPAL	P. Venu Gopal
47	19811A0449	PULAVARTHI SATYANARAYANA	P. Satyanarayana
48	19811A0450	PYLA MURARI	P. Murari
51	19811A0451	SRIGAKOLAPU PRIYA NIKITHA	S. Priya Nikitha

Principal

Avanthi Institute of Engg. & Technology  
Tamararam, Makavarapalem Md.,  
Visakhapatnam District., Pin: 531113





# AVANTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY

(Approved By AICTE, Permanently Affiliated to JNT University, Kakinada,  
Accredited by NAAC & Recognized Under 2(f) and 12 (b) by UGC, New Delhi)  
Tamaram, Makavarapalem, Narsipatnam(R D), Visakhapatnam Dist-531113

## Orientation Programs organized for Non-Teaching

A.Y:2019-20

S.No	Name of the program	Resource person	Program Date	No of participants
11	A One Day program on "Best practices in Office Administration."	Dr B Lova Baliji, HoD, MBA, AIET, Makavarapalem.	20-02-2020	13

  
IQAC Coordinator





**AVANTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY**  
(Approved By AICTE, Permanently Affiliated to JNT University, Kakinada,  
Accredited by NAAC & Recognized Under 2(f) and 12 (b) by UGC, New Delhi)  
Tamaram, Makavarapalem, Narsipatnam(R D), Visakhapatnam Dist-531113


Date: 18/02/2020

**CIRCULAR**

A One day program on “Best practices in Office Administration” is scheduled on 20<sup>th</sup> Feb 2020. All the Non –Teaching staff should attend the JKC Lab without fail.

**Resource Person – Dr. B Lova Balihi, MBA- HOD, AIET**

Copy to: To All the Non –Teaching staff, AIET

  
Principal  
Principal  
Avanthi Institute of Engg. & Technology  
Tamaram, Makavarapalem Md.,  
Visakhapatnam District., Pin: 531113



## AVANTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY

(Approved By AICTE, Permanently Affiliated to JNT University, Kakinada,  
Accredited by NAAC & Recognized Under 2(f) and 12 (b) by UGC, New Delhi)  
Tamaram, Makavarapalem, Narsipatnam(R D), Visakhapatnam Dist-531113

### A One Day program on "Best practices in Office Administration."

Resource person: Dr B Lova Baliji, HoD, MBA, AIET, Makavarapalem.

Date: 20-02-2020

SLNO.	Name	Designation	Signature
1	S DANIEL RAJU	AO	
2	SHAIK MADEENA	Sr.Accountant	
3	S BAPI NAIDU	Electrician	
4	S.APPALA RAO	Exam cell Assistant	
5	N. SRINIVASA RAO	Exam cell Assistant	
6	P SRAVAN KUMAR	Accountant	
7	R MARKANDEYULU	Bus In charge	
8	K. APPA RAO	Office Assistant	
9	S NIRMALA KUMARI	warden	
10	G NAGARJUNA	Lab Assistant.	
11	SRINIVASA SARMA	Office Assistant	
12	P. APPA RAO	Floor In charge	
13	P AKHILESWAR	Lab Assistant.	

Principal  
Principal

Avanthi Institute of Engg. & Technology  
Tamaram, Makavarapalem Md.,  
Visakhapatnam District., Pin: 531113